

## IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) A microchip for a personal computer, comprising:  
a plurality of dies, [each] at least two of the dies made by a separate fabrication process  
and assembled into a package with the separate die sections connected directly;  
a wireless network connection capable of coupling the personal computer to at least one  
other personal computer via a network;  
a firewall comprising a hardware and/or a firmware component; and  
a power management function component capable of managing power on the microchip.
2. (Currently Amended) The microchip according to claim 1, wherein the separate die  
sections are connected by at least one interconnect[s] that [are] is widened compared to the  
~~circuits~~ interconnect lines of the die.
3. (New) The microchip according to claim 1, wherein the microchip further  
comprises an encryption component.
4. (New) The microchip according to claim 1, wherein the microchip further  
comprises an electro-mechanical (MEMS) component.
5. (New) The microchip according to claim 1, wherein the microchip further  
comprises a microprocessor with a control unit and at least two or four or 8 or 16 or 32 or 64  
or 128 or 256 or 512 or 1024 processing units, the control unit allowing a user of the personal  
computer to control the processing units.
6. (New) The microchip according to claim 1, wherein the microchip further  
comprises an optical omniguide or a radio omniguide.
7. (New) The microchip according to claim 1, wherein the microchip further  
comprises at least one lense.

8. (New) The microchip according to claim 2, wherein the interconnects utilize multiplexing.
9. (New) The microchip according to claim 1, wherein the microchip further comprises at least one field-programmable gate array.
10. (New) The microchip according to claim 1, wherein the microchip further comprises at least one photovoltaic cell.
11. (New) The microchip according to claim 1, wherein the microchip further comprises at least one Faraday Cage.
12. (New) The microchip according to claim 1, wherein the microchip further comprises at least one laser.
13. (New) The microchip according to claim 5, wherein at least one of the processing units includes a firewall.
14. (New) The microchip according to claim 5, wherein at least one of the processing units has a connection located on the microchip to another of the processing units on the microchip.
15. (New) The microchip according to claim 5, wherein at least one of the processing units has a connection located on the microchip to a control unit on the microchip.
16. (New) The microchip according to claim 5, wherein at least one of the processing units has a connection located on the microchip to a memory component on the microchip that is shared with another of the processing units on the microchip.
17. (New) The microchip according to claim 5, wherein at least one of the processing units has a connection located on the microchip to a memory component on the microchip that is shared with a control unit on the microchip.
18. (New) The microchip according to claim 1, wherein the microchip further comprises a digital signal processor (DSP).

19. (New) The microchip according to claim 5, wherein the firewall permits access by the network to at least one of the processing units while at the same time denying access to at least another of the processing units.

20. (New) The microchip according to claim 5, wherein the firewall permits access by the network to at least one of the processing units while at the same time denying access to at least the control unit.